



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
(UGC – AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300 (Engg.)

NBA Accredited - B.Tech. (CIVIL, CST, CSE, ECE, EEE, MECH), MBA & MCA

Examination Section

B. Tech – II Year II Semester (R14) (Last Chance) MOOCs Supplementary End Semester Examinations – July 2026
(For 2014, 2015, 2016 & 2017 Regular & 2015, 2016, 2017 & 2018 Lateral Entry Admitted Batches only)
Academic Year – 2025 - 26



Timings: FN: 10:00 AM to 01:00 PM

DATE / DAY	CE	EEE	ME	ECE	CSE	CSIT/IT
27.08.2026 Thursday	2014 Admitted Batch					
	Probability & Statistics 14MAT104-M1	Probability & Statistics 14MAT104-M1	Manufacturing Process Technology - Part I 14ME110-M1	Probability & Statistics 14MAT104-M1	Design & Analysis of Algorithms – 14CSU109-M1	-
	2015 Admitted Batch					
	Digital Land Surveying And Mapping 14CE108-M1	Introduction to Electronics 14EE E109-M1	Introduction to Fluid Mechanics- 14ME106-M1	Basic Electronics – 14ECE107-M1	Computer Architecture – 14CSU108-M1	-
	-	Analog Circuits- 14EEE109-M2	Machine Design Part I-14ME107-M1	-	Design & Analysis of Algorithms-14CSU109-M1	-
	-	-	Steam and Gas Power System 14ME109-M1	-	-	-
	-	-	Manufacturing Process Technology - Part I & II- 14ME110-M2	-	-	-
	2016 Admitted Batch					
	Digital Land Surveying And Mapping (DLS&M) 14CE108-M1	Electromagnetic Theory 14EEE106-M1	Manufacturing Process Technology I & II 14ME110-M2	Principles of Signals & Systems 14ECE105-M1	Computer Organization and Architecture: A Pedagogical Aspect 14CSU108-M2	-
	2017 Admitted Batch					
	Digital Land Surveying And Mapping(DLS&M) -14CE108-M1	Microprocessors & Microcontrollers 14eee107-M1	Manufacturing Process Technology 14ME110-M3	Principles of Signals & Systems 14ECE105-M1	Computer Architecture & Organization 14CSU108-M3	Computer Architecture & Organization 14CSIT108-MI/14IT108-MI

- Note:** (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.
(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.



CONTROLLER OF EXAMINATIONS

Date: 15.07.2026

Copy to: P.A to Principal

All the HODs for circulation among the staff members and display on notice board,
Assistant Controllers of Examinations,
Senior Administrative Officer,
Senior Placement Officer.
Transport Incharge



PRINCIPAL

**Principal
Madanapalle Institute of
Technology & Science
MADANAPALLE**